

**REMARKS**

Claims 2-9, 11-16, 22-35, and 37-40 remain in the application for consideration of the Examiner with Claims 1 and 41 standing cancelled.

Reconsideration and withdrawal of the outstanding rejections are respectfully requested in light of the above amendments and following remarks.

Claims 1 and 41 were rejected under 35 U.S.C. § 102(b) as being anticipated by Butler.

The cancellation of Claims 1 and 41 obviates the rejection of these claims.

It is respectfully submitted that Butler does not disclose or suggest the presently claimed invention including the output stage circuit including a controlled resistive circuit including at least one control resistive element coupled to the pair of output transistors.

Butler discloses element 32 which is not in the output stage.

Applicants appreciate the indication that Claims 2-9 would be allowable if rewritten in independent form including all the limitations of the base claim and any intervening claims.

These claims are now in independent form.

Applicants appreciate the indication that Claims 11-16, 22-35, and 37-40 are allowed.

In light of the above, it is respectfully submitted that the present application is in condition for allowance, and notice to that effect is respectfully requested.

While it is believed that the instant response places the application in condition for allowance, should the Examiner have any further comments or suggestions, it is respectfully requested that the Examiner contact the undersigned in order to expeditiously resolve any outstanding issues.

To the extent necessary, Applicant petitions for an Extension of Time under 37 CFR 1.136. Please charge any fees in connection with the filing of this paper, including extension of time fees, to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,



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